



11-12-04

2183

SPW

Inventor application of:	G. Glenn Henry Thomas C. McDonald
Serial No.:	09/849736
Filed:	05/04/01
Docket:	CNTR.2021
For:	A SPECULATIVE BRANCH TARGET ADDRESS CACHE THAT OUTPUTS THE TARGET ADDRESS OF A BRANCH INSTRUCTION AND SPECULATIVE BRANCH INFORMATION IN RESPONSE TO A FETCH ADDRESS WHETHER OR NOT THE BRANCH IS PRESENT IN THE INSTRUCTION CACHE LINE INDEXED BY THAT ADDRESS

**INFORMATION DISCLOSURE STATEMENT
SUBMITTED UNDER 37 CFR 1.97(D) AND 1.97 (E)(2)**

Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the Examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims and return an initialed copy of each form to the undersigned.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 C.F.R. § 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR § 1.56(b) or is available as a reference under 35 U.S.C. § 102 *et seq.* Applicant reserves the right to swear behind or otherwise disprove the alleged "prior" nature of any art cited should the facts support and the situation warrant such an action.

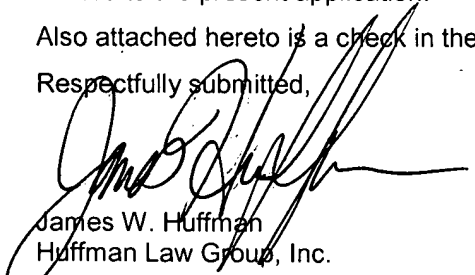
It is believed that this disclosure complies with the requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98, and the Manual of Patent Examining Procedures § 609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

A copy of each document is enclosed. Some of the documents may have markings thereon. No significance is intended to be attached to the markings.

No item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of the information disclosure statement. The information cited in the information disclosure statement was cited by the United States Patent Office in one or more applications that are related to the present application.

Also attached hereto is a check in the amount of **\$180.00** for the fee set forth in § 1.17(p).

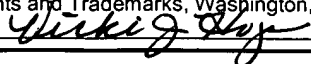
Respectfully submitted,

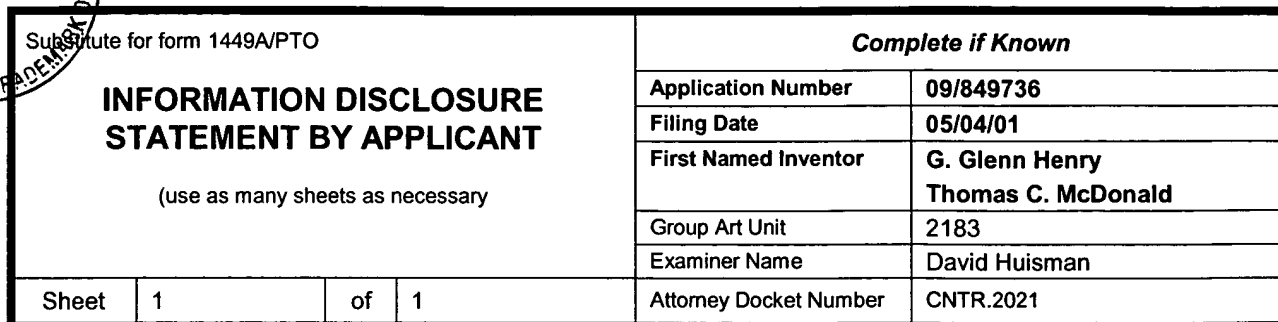


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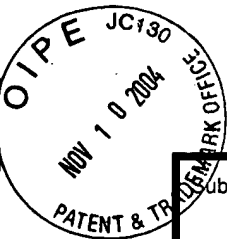
11/15/2004 CCHAU1 00000058 09849736
01 FC:1806 180.00 OP

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Date of Deposit <u>11-10-04</u> . I hereby certify that this paper is being deposited with the U.S. Postal Service Express Mail Post Office to Addressee Service under 37 C.F.R. §1.10 on the date shown above and is addressed to the U.S. Commissioner of Patents and Trademarks, Washington, D.C. 20231.	
By:	

[illegible][illegible]

Examiner Signature		Date Considered	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	09/849736
				Filing Date	05/04/01
				First Named Inventor	G. Glenn Henry Thomas C. McDonald
				Group Art Unit	2183
				Examiner Name	David Huisman
Sheet	2	of	3	Attorney Docket Number	CNTR.2021

OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	BA	"Microarchitecture support for reducing Branch Penalty in a Superscaler Processor", pp 208-216; Mamoru Sakamoto et al., Mitsubishi Electric Corp., 4-1 Mizuhara, Itami, Hyogo 664, Japan, 1996 IEEE.	
	BB	Microprocessor Report; Intel's P6 Uses Decoupled Superscalar Design, Vol. 9, No. 2, February 16, 1995.	
	BC	Microprocessor Report; MicroDesigns Resources, August 23, 1999.	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

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